



Atty. Docket No.: 3359-Z

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Khader S. Abdel-Hafez et al

Application No. 10/691,966

Group Art Unit 2858

Filed: October 24, 2003

Examiner John P. Trimmings

For: METHOD AND APPARATUS FOR TESTING ASYNCHRONOUS SET/RESET  
FAULTS IN A SCAN-BASED INTEGRATED CIRCUIT

**AMENDMENT AFTER FINAL**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed April 5, 2005, please  
amend the above-identified application as follows: